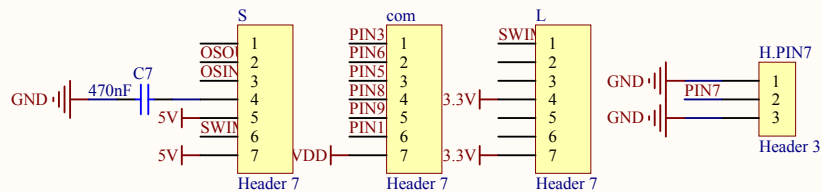
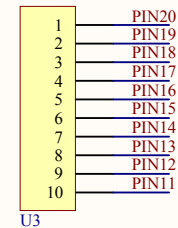
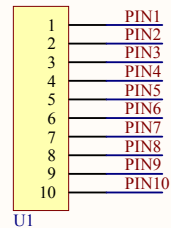


PIN1	1	PD4 (PC3)	PD3(PC2)	20	PIN20
PIN2	2	PD5(PC4/CCO)	PD2(PC1)	19	PIN19
PIN3	3	PD6(SWIM)	PD1/SWIM(PC0)	18	PIN18
PIN4	4	NRST(PA1/NRST)	PC7(PB7)	17	PIN17
PIN5	5	PA1/OSCIN(PA2)	PC6(PB6)	16	PIN16
PIN6	6	PA2/OSCOUT(PA3)	PC5(PB5)	15	PIN15
PIN7	7	VSS	PC4(PB4)	14	PIN14
PIN8	8	VCAP(VDD)	PC3(PB3)	13	PIN13
PIN9	9	VDD(PD0)	PB4(PB2)	12	PIN12
PIN10	10	PA3(PB0)	PB5(PB1)	11	PIN11

STM8S103FxP6/STM8L101FxP6



Title		
Size	Number	Revision
A4		
Date:	2015/4/13	Sheet of
File:	E:\林新武\SVN\STM8-TSSOP20.SchDoc	Drawn By: